

**SPECIFICATION
FOR
TFT MODULE**

MODULE NO. :TFT-0176-CT

Customer Approval:

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	SIGNATURE	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
O	2015-11-04	First release	Preliminary

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1. GENERAL INFORMATION

No.	Item	Contents	Unit
1	LCD size	5.0 inch (Diagonal)	/
2	LCD type	TN/Normally white/Transmissive(Anti-glare)	/
3	Viewing direction(eye)	12 O'clock	/
4	Gray scale inversion direction	6 O'clock	/
5	Resolution(H*V)	800 *480 Pixels	/
6	Module size (L*W*H)	128.8*88.3*4.45	mm ³
7	Active area (L*W)	108*64.8	mm ²
8	Pixel pitch (L*W)	0.135*0.135	mm ²
9	Interface type	RGB interface	/
10	Module power consumption	TBD	W
11	Back light type	LED	/
12	Driver IC	ILI6122+ILI5960 OR COMPATIBLE	/
13	Weight	TBD	g

2.ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Max	Unit
Power supply input voltage (LCM)	VDD	-0.3	5.0	V
Backlight current (normal temp.)	ILED	-	50	mA
Operation temperature	Top	-20	70	°C
Storage temperature	Tst	-30	80	°C
Humidity	RH	-	90%(Max60 °C)	RH

3. ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS (at Ta=25°C)

Item	Symbol	Min	Typ	Max	Unit	Note
Power supply input voltage (LCM)	VDD	3.0	3.3	3.6	V	
I/O logic voltage	VDDIO	N/A	N/A	N/A	V	
Input voltage 'H' level	VIH	0.7VDDI	-	VDDI	V	
Input voltage 'L' level	VIL	VSS	-	0.3VDDI	V	
Power supply current	IVDD	-	TBD	-	mA	

4. BACKLIGHT CHARACTERISTICS

(at Ta=25°C,RH=60%)

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED forward voltage	VF	-	19.2	20.4	V	IF=20*2mA
LED forward current	IF	-	40	-	mA	
LED power consumption	PLED	-	0.768	-	W	Note1
Number of LED	-		4		PCS	
Connection mode	-	6 in series	2 in parallel		/	
LED life-time	-	20000	-	-	Hrs	Note2

Note1.Calculator Value for reference: IF*VF = PLED

Note2.The LED Life-time define as the estimated time to 50% degradation of initial brightness at Ta=25°C and IF =40mA. The LED lifetime could be decreased if operating IF is larger than 40mA

5. TOUCH PANEL CHARACTERISTICS (at Ta=25°C)

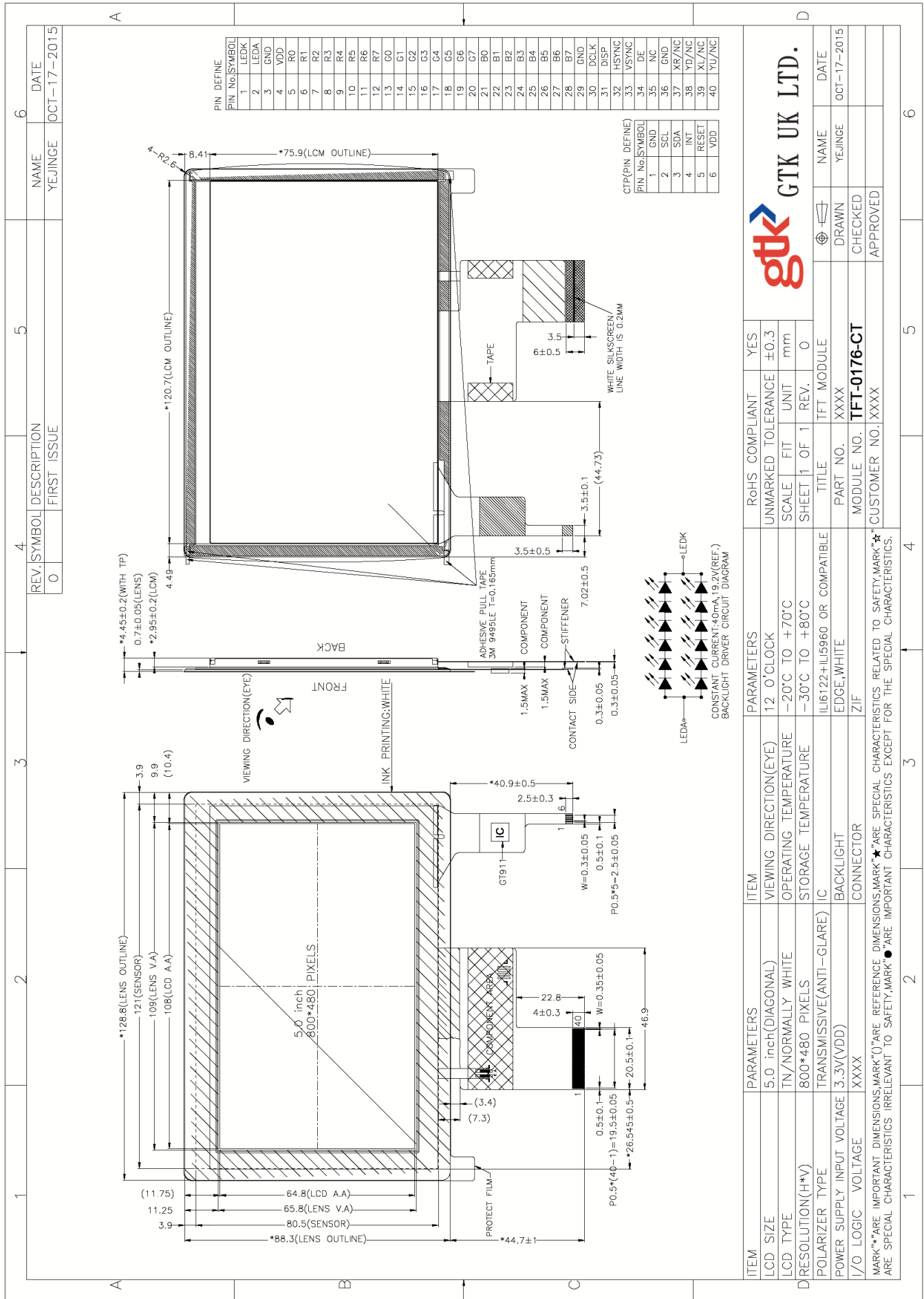
FPC Design	Item	Description	Note
[√] COF	IC solution on TP Model	GT911	
	Touch Count Max	5 point	
	Display Resolution*	800*480	
	Interface Type *	I2C	
	I2C Slave Address*	0x5D	
	Origin of Coordinate*	top left corner	
[] COB	IC solution on Broad*		
	Driving Channels		
	Sensing Channels		

Parameter	Min	Typ	Max	Unit
Interface Signal Voltage*	1.8	3.3	3.6	V
Power Voltage*	2.6	3.3	3.6	V
Power ripple*	---	---	50	MV

Note1: The detail refer to the Specification For IC

Note2: '*' means that the item is optional according to the product requirement

6. EXTERNAL DIMENSIONS



NAME	DATE
YEJINGE	OCT-17-2015
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7. ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark	Note
Response time	Tr+ Tf	-	-	25	50	ms	FIG.1	Note 4
Contrast ratio	Cr		400	500	-	-	FIG.2	Note 1
Surface luminance	Lv	$\theta=0^\circ$	190	240	-	cd/m ²	FIG.2	Note 2
Luminance uniformity	-	$\theta=0^\circ$	75	80	-	%	FIG.2	Note 3
NTSC	-	$\theta=0^\circ$	-	TBD	-	%	FIG.2	Note 5
Viewing angle range	θ	$\varnothing = 90^\circ$	60	70	-	deg	FIG.3	Note 6
		$\varnothing = 270^\circ$	40	50	-	deg	FIG.3	
		$\varnothing = 0^\circ$	60	70	-	deg	FIG.3	
		$\varnothing = 180^\circ$	60	70	-	deg	FIG.3	
CIE (x, y) chromaticity	Red x	$\theta=0^\circ$ $\varnothing=0^\circ$ Ta=25°C	Typ -0.04	TBD	Typ +0.04	-	FIG.2 CIE1931	Note 5
	Red y			TBD		-		
	Green x			TBD		-		
	Green y			TBD		-		
	Blue x			TBD		-		
	Blue y			TBD		-		
	White x			TBD		-		
	White y			TBD		-		

Note1. Definition of contrast ratio

Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG.2

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}$$

Note2. Definition of surface luminance

Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG.2

Lv = Average Surface Luminance with all white pixels(P1, P2, P3, P4, P5, P6, P7, P8, P9)

Note3. Definition of luminance uniformity

The luminance uniformity in surface luminance (δ WHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG.2

$$Y_u = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}$$

Note4. Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

Note5. Definition of color chromaticity (CIE1931)

CIE (x, y) chromaticity, The x, y value is determined by screen active area center position P5, For more information see FIG.2

Note6. Definition of Viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3

For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-5or BM-7 photo detector or compatible.

Note: For TFT module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

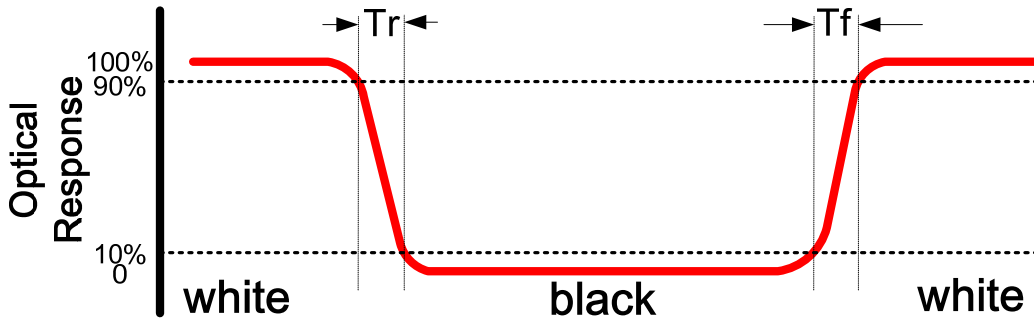


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

Size: $S \leq 5"$ (see Figure a)

A : 5 mm B : 5 mm

H, V : Active Area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position: see Figure a.

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c)

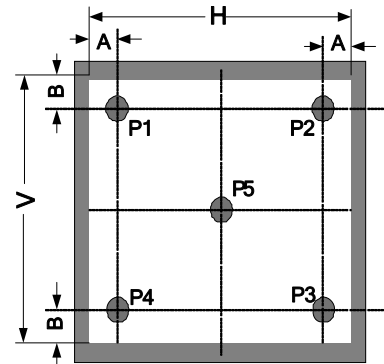


Figure a

Size: $5" < S \leq 12.3"$ (see Figure b)

H, V : Active Area

Light spot size $\varnothing = 5\text{mm}$ (BM-5) or $\varnothing = 7.7\text{mm}$ (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens

test spot position: see Figure b

measurement instrument : TOPCON's luminance meter BM-5 or BM-7 or compatible (see Figure c)

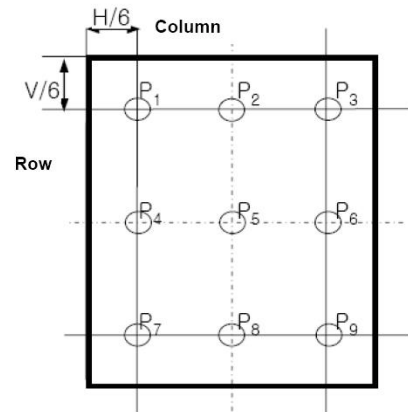


Figure b

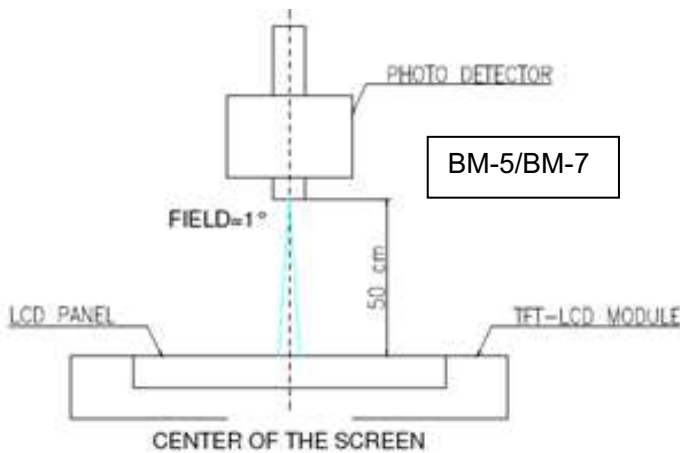
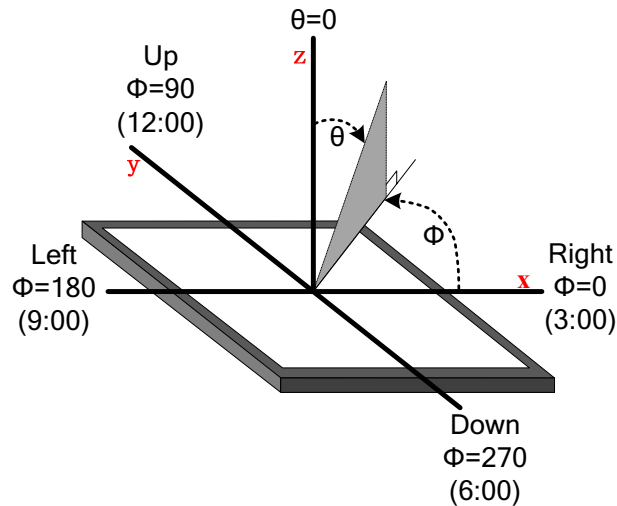


Figure c

FIG.3. The definition of viewing angle



8. INTERFACE DESCRIPTION

LCM Interface description

Interface No.	Name	I/O or connect to	Description
1	LEDK	P	Power for LED backlight(Cathode)
2	LEDA	P	Power for LED backlight(Anode)
3	GND	P	Ground
4	VDD	P	Power for LCD
5-12	Red(0-7)	I	Red data
13-20	Green(0-7)	I	Green data
21-28	Blue(0-7)	I	Blue data
29	GND	I	Ground
30	DCLK	I	Data Enable signal ;normally pull low
31	DISP	I	Display on/off
32	HSYNC	I	Horizontal sync input.
33	VSYNC	I	Vertical sync input
34	DE	I	Data enable
35	NC	/	/
36	GND	P	Power Ground
37	XR/NC	/	/
38	YD/NC	/	/
39	XL/NC	/	/
40	YU/NC	/	/

CTP interface description

Interface NO.	NAME	I/O or connect to	DESCRIPTION
1	GND	P	Ground
2	SCL	I	Serial interface clock
3	SDA	I/O	Serial interface data
4	INT	O	State change interrupt
5	RST	I	Reset low
6	VDD	P	Power Supply

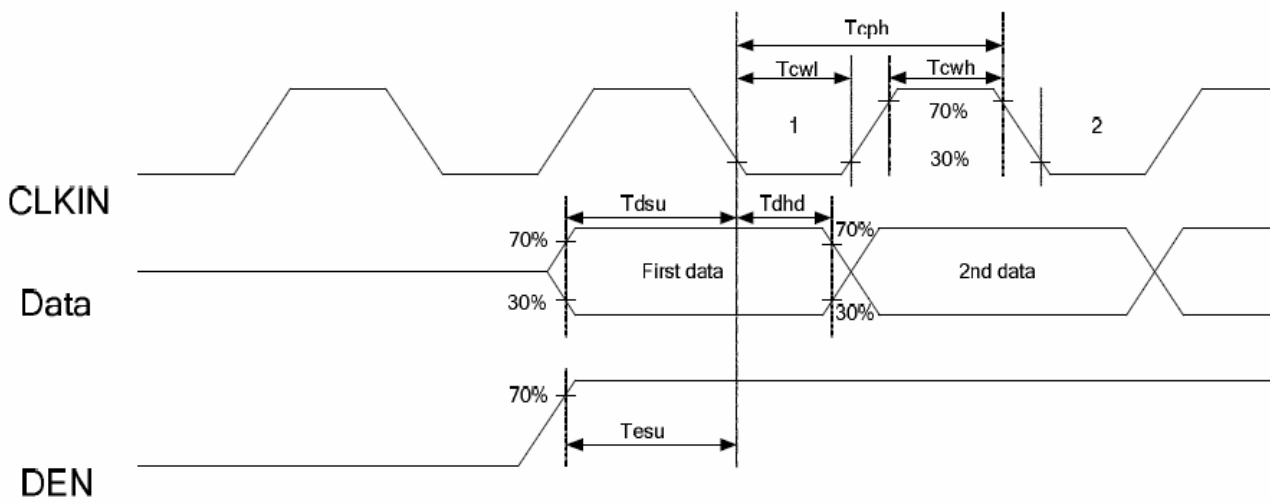
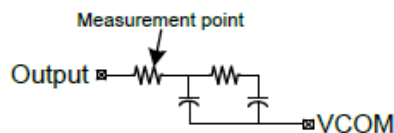
9. AC CHARACTERISTICS

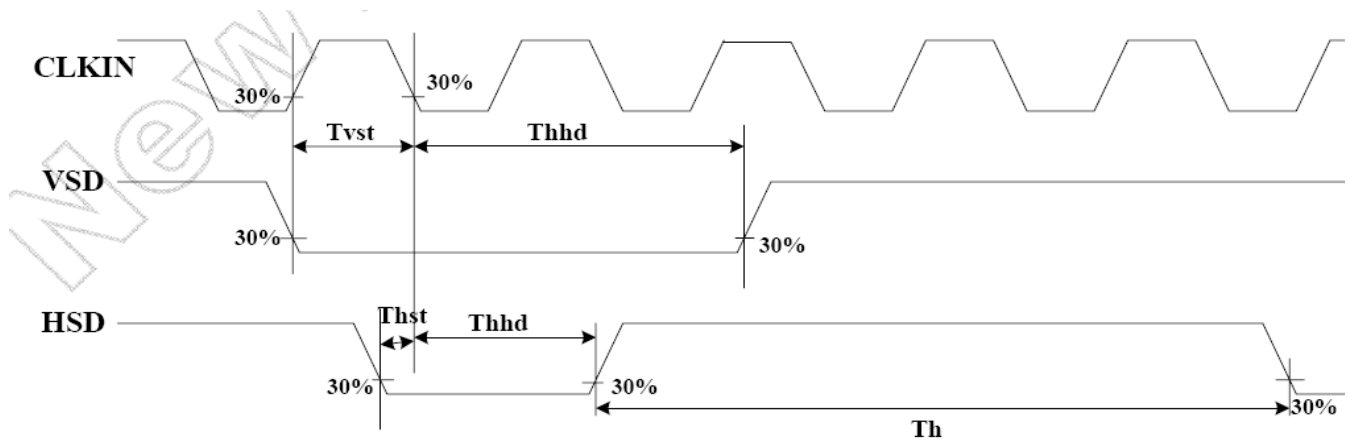
Parameter	Symbol	Spec			Unit	Conditions
		Min.	Typ.	Max.		
VDD Power ON slew rate	t_{POR}	--	--	20	ms	0V ~ 0.9VDD
RSTB pulse width	t_{RST}	10	--	--	us	CLKIN=50MHz
CLKIN cycle time	t_{CPH}	20	--	--	ns	
CLKIN pulse duty	t_{CWH}	40	50	60	%	
VSD setup time	t_{VST}	8	--	--	ns	
VSD hold time	t_{VHD}	8	--	--	ns	
HSD setup time	t_{HST}	8	--	--	ns	
HSD hold time	t_{HHD}	8	--	--	ns	
Data setup time	t_{DST}	8	--	--	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN
Data hold time	t_{DHD}	8	--	--	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN
DE setup time	t_{EST}	8	--	--	ns	
DE hold time	t_{EHD}	8	--	--	ns	
Output stable time	t_{SST}	--	--	6	us	10% to 90% target voltage. CL=120pF, R=10K Ω
CLKIN frequency	f_{CLK}	--	40	50	MHz	VDD=3.0 ~ 3.6V
CLKIN cycle time	t_{CLK}	20	25	--	ns	
CLKIN pulse duty	t_{CWH}	40	50	60	%	T_{CLK}
Time from HSD to Source output	t_{HSO}	--	20	--	CLKIN	
Time from HSD to LD	t_{HLD}	--	20	--	CLKIN	Note (2)
Time from HSD to STV	t_{HSTV}	--	2	--	CLKIN	
Time from HSD to CKV	t_{HCKV}	--	20	--	CLKIN	
Time from HSD to OEV	t_{HOEV}	--	4	--	CLKIN	
LD pulse width	t_{WLD}	--	10	--	CLKIN	Note (2)
CKV pulse width	t_{WCKV}	--	66	--	CLKIN	
OEV pulse width	t_{WOEV}	--	74	--	CLKIN	

Note: (1) VDD=3.0 ~ 3.6V, VDDA=6.5~13.5V, DGND=AGND=0V, Ta=-20~+85 $^{\circ}$ C

(2) The contents of the data register are transferred to the latch circuit at the rising edge of LD. Then the gray scale voltage is output from the device at the falling edge of LD.

(3) Output loading condition :

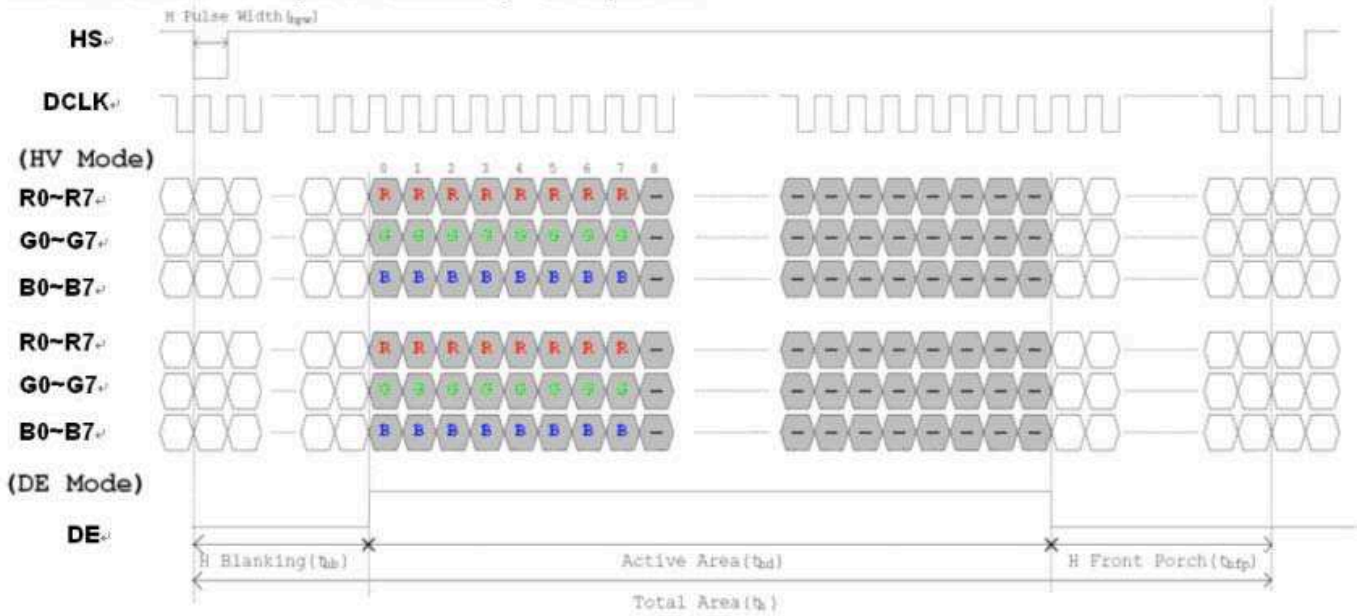




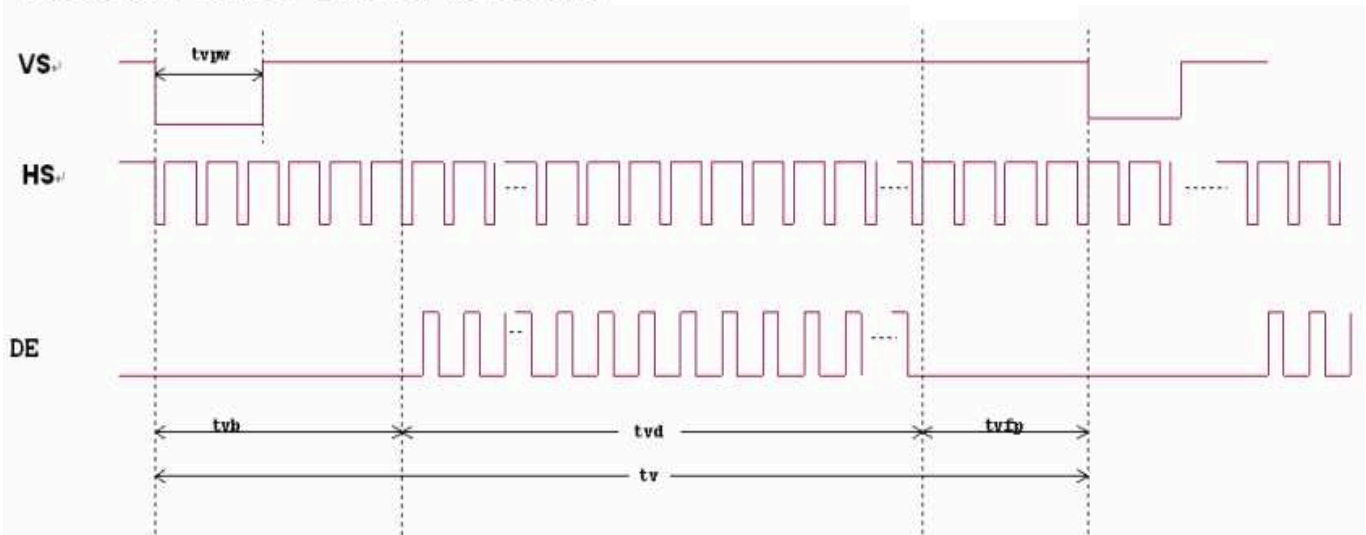
Horizontal Input Timing						
Parameter	Symbol	Value			Unit	
		Min.	Typ.	Max.		
Horizontal display area	t_{HD}	--	800	--	CLKIN	
CLKIN frequency	f_{CLK}	--	33.3	50	MHz	
1 Horizontal line period	t_H	862	1056	1200	CLKIN	
HSD pulse width	t_{HPW}	Min.	--	1	CLKIN	
		Typ.	--	--	CLKIN	
		Max.	--	40	CLKIN	
HSD back porch	SYNC	t_{HBP}	46	46	46	CLKIN
HSD front porch	SYNC	t_{HFP}	16	210	354	CLKIN

Vertical Input Timing					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	t_{VD}	--	480	--	HSD
VSD period time	t_V	510	525	650	HSD
VSD pulse width	t_{VPW}	1	--	20	HSD
VSD back porch	t_{VBP}	23	23	23	HSD
VSD front porch	t_{VFP}	7	22	147	HSD

Horizontal input timing diagram.



Vertical input timing diagram.



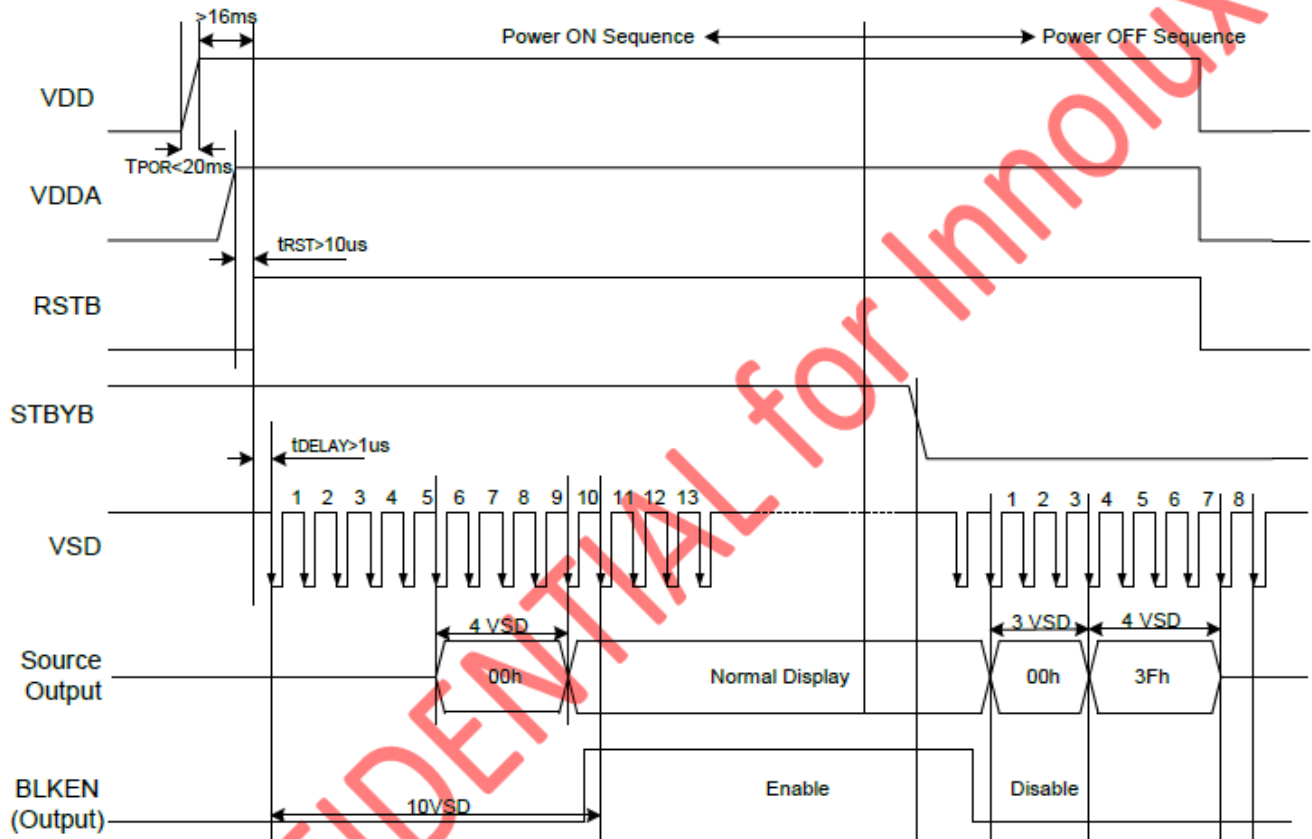
10. POWER SEQUENCE

To prevent the device damage from latch up, the power ON/OFF sequence shown below must be followed.

Power ON: VDD, DGND → VDDA, AGND → V1 to V14

Power OFF: V1 to V14 → VDDA, AGND → VDD, DGND

In order to prevent ILI6122 from power ON reset fail, the rising time (t_{POR}) of the digital power supply VDD should be maintained within given specifications. The power ON/OFF timing sequence is illustrated as below:



Note: For prevent anormal operation, t_{RST} must be longer than 10us during Power ON sequence.

11. RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	80±2°C/240 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Current changing value before test and after test is 50% larger ; 2. function defect:Non-display ,abn ormal-display,missing lines,Short lines , ITO corossion ; 3.visual defect:Air bubble in the LCD,Sealleak,Glass crack。
2	Low Temperature Storage	-30±2°C/240 hours	
3	High Temperature Operating	70±2°C/120 hours	
4	Low Temperature Operating	-20±2°C/120 hours	
5	Temperature Cycle	-20±2°C~25~70±2°C*10cycles (30min.) (5min.) (30min.)	
6	Damp Proof Test	50°C*90% RH/120 hours	
7	Vibration Test	Frequency : 10Hz~55Hz~10Hz Amplitude :1.5mm, X ,Y ,Z direction for total 3hours (Packing condition)	
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time	
Remark: 1.The test samples should be applied to only one test item. 2.Sample size for each test item is 3~5pcs. 3.For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used. 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part. 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has. 6.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.			

12. INSPECTION CRITERION

12.1 Description

This specification is made to be used as the standard acceptance/rejection criteria for TFT LCM Product.

1. Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1 : 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

Minor defect: AQL 1.5

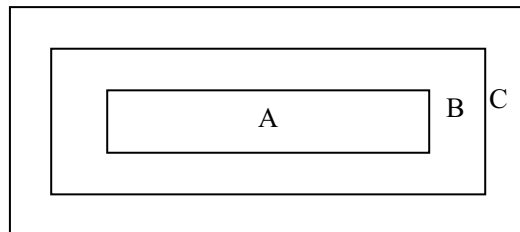
2. Inspection condition

● Viewing distance for cosmetic inspection is about $30\pm 5\text{cm}$ with bare eyes, and under an environment $600\sim 1000\text{lux}$ for visual inspection and $0\sim 200\text{lux}$ for function test., all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature $18\sim 28^\circ\text{C}$ and normal humidity $60\pm 15\%\text{RH}$).

● Driving voltage

The Vop value from which the most optical contrast can be obtained near the specified Vop in the specification (Within $\pm 0.5\text{V}$ of the typical value at 25°C .).

3. Definition of inspection zone in LCD



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A+Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

12.2 Inspection criterion

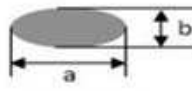
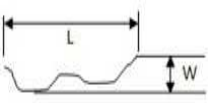
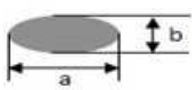
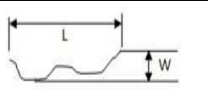
12.2.1 Function defect

Items to be inspected	Inspection criterion	Classification of defects
All functional defects	1) No display 2) Display abnormally 3) Missing vertical , horizontal segment 4) Short circuit 5) Back-light no lighting, flickering and abnormal lighting. 6) obvious striation 7) Current beyond specification value	MA
Missing	Missing component	
Outline dimension	Overall outline dimension exceed the drawing is not allowed.	

12.2.2 LCD pixel defect (bad dot) (defect type:MI)

Checking item	Judgment criterion		
	Item/LCD size	S ≤5.0 Inch	5.0 < S≤7.0 Inch
Color bad dot-bright dot(R、 G、 B)	1	2	3
two adjacent bright point	0	1	2
three or more adjacent point	0	0	0
total points for bad dot-bright dot	1	2	5
Bad dot-dark dot	2	4	5
two adjacent dark point	1	2	3
three or more adjacent point	0	1	1
total points for bad dot -dark dot	3	6	7
patch bright dot	Invisible with ND5%,it is OK.		

12.2.3 dot and line defect (defect type:MI)

Checking item	Judgment criterion				Figure	
	Diameter(mm)\LCD Size	S ≤5.0 Inch	5 < S≤7 Inch	7 < S≤12.3 Inch		
Dot defect	D≤0.1	allowed	allowed	allowed	 D=(a+b)/2	
	0.1 < D≤0.2	4	allowed	allowed		
	0.2 < D≤0.3	0	5	6		
	0.3 < D≤0.5	0	0			
	D > 0.5	0	0	0		
the distance between the two defect dot:DS≥5mm						
line defect	Length(mm)	width(mm)	Judgment criterion			
	disregard	W≤0.05	allowed	allowed	allowed	
	L≤5	0.05 < W≤0.1	4	5	7	
	L > 5	W > 0.1	0	0	0	
Concave point and air bubble for polarizer	LCD Size(mm)		Judgment criterion			 D=(a+b)/2
	D≤0.3		allowed	allowed	allowed	
	0.3 < D≤1.0		3	4	5	
	1.0 < D≤1.5		1	2	3	
D > 1.5		0	0	0		
Fold mark, linear scar for polarizer	Length (mm)	width (mm)	Judgment criterion			
	disregard	W≤0.05	allowed	allowed	allowed	
	1 < L≤5	0.05 < W≤0.2	3	4	5	
	L > 5	W > 0.2	0	0	0	
Notes:1.If the fold mark and linear scar for polarizer is visible with operating condition,the defect is judged with line judge ; 2.If the fold mark and linear scar for polarizer is visible with non-operating condition,the defect is judged with the above judgment standard.						

12.2.4 Corner and others crack for LCD (defect type:MI)

Checking item	Judgment criterion	Figure
electric conduction crack	$X \leq 3.0\text{mm}, Y \leq 1/4w, Z \leq t, N \leq 2$	
corner crack	$X \leq 3.0\text{mm}, Y \leq 3.0\text{mm}, Z \leq t, N \leq 3$ Corner crack extended to ITO PIN, none allowed	
surface crack	$X \leq 1.5\text{mm}, Y \leq 1.0\text{mm}, Z \leq t, N \leq 4$	

12.2.5 Module Cosmetic Criteria (defect type:MI)

Item	Judgment Criterion
Difference in Spec.	None allowed
Pattern peeling	No substrate pattern peeling and floating
Soldering defects	No soldering missing No soldering bridge No cold soldering Notes:detail judgment referring to IPC-A-610 grade II
Resist flaw on Printed Circuit Boards	visible copper foil (0.5mm or more) on substrate pattern, none allowed
Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed 0.2mm)
Stain	No stain to spoil cosmetic badly
Plate discoloring	No plate fading, rusting and discoloring
Newton ring	Referring to limited sample
Mura	Invisible with 5%ND,allowed
Light leaks	Referring to limited sample

13. HANDLING PRECAUTIONS

13.1 Mounting method

The LCD module consists of two thin glass plates with polarizers which easily be damaged. And since the module is so constructed as to be fixed by utilizing fitting holes in the printed circuit board. Extreme care should be needed when handling the LCD modules.

13.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[recommended below] and wipe lightly

- .Isopropyl alcohol
- .Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- .Water
- .Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- .Soldering flux
- .Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happens by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

13.3 Caution against static charge

The LCD module uses C-MOS LSI drivers, so we recommend that you:

Connect any unused input terminal to V_{dd} or V_{ss}, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

13.4 Packing

Module employs LCD elements and must be treated as such.

- .Avoid intense shock and falls from a height.
- .To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

13.5 Caution for operation

- .It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life.
- .An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- .Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- .If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- .A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

13.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- .Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.
 - .Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it . And with no desiccant.
 - .Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
 - .Storing with no touch on polarizer surface by the anything else.
- It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

13.7 Safety

- .It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- .When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

14. PRECAUTION FOR USE

14.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

14.2 On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- .When a question is arisen in this specification
- .When a new problem is arisen which is not specified in this specifications
- .When an inspection specifications change or operating condition change in customer is reported to AVD , and some problem is arisen in this specification due to the change
- .When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

15. PACKING SPECIFICATION

Please consult our technical department for detail information.